

# MBT3946DW1T1G, SMBT3946DW1T1G

## Complementary General Purpose Transistor

The MBT3946DW1T1G device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-363-6 surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

### Features

- $h_{FE}$ , 100–300
- Low  $V_{CE(sat)}$ ,  $\leq 0.4$  V
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Table 1. MAXIMUM RATINGS

| Rating                                           | Symbol    | Value                     | Unit |
|--------------------------------------------------|-----------|---------------------------|------|
| Collector – Emitter Voltage<br>(NPN)<br>(PNP)    | $V_{CEO}$ | 40<br>–40                 | Vdc  |
| Collector – Base Voltage<br>(NPN)<br>(PNP)       | $V_{CBO}$ | 60<br>–40                 | Vdc  |
| Emitter – Base Voltage<br>(NPN)<br>(PNP)         | $V_{EBO}$ | 6.0<br>–5.0               | Vdc  |
| Collector Current – Continuous<br>(NPN)<br>(PNP) | $I_C$     | 200<br>–200               | mAdc |
| Electrostatic Discharge                          | ESD       | HBM Class 2<br>MM Class B |      |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 2. THERMAL CHARACTERISTICS

| Characteristic                                                 | Symbol          | Max         | Unit               |
|----------------------------------------------------------------|-----------------|-------------|--------------------|
| Total Package Dissipation (Note 1)<br>$T_A = 25^\circ\text{C}$ | $P_D$           | 150         | mW                 |
| Thermal Resistance,<br>Junction-to-Ambient                     | $R_{\theta JA}$ | 833         | $^\circ\text{C/W}$ |
| Junction and Storage Temperature Range                         | $T_J, T_{stg}$  | –55 to +150 | $^\circ\text{C}$   |

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.

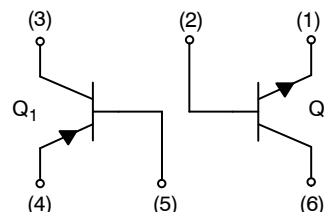


ON Semiconductor®

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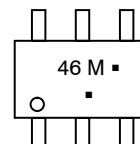
SOT-363/SC-88  
CASE 419B  
STYLE 1



MBT3946DW1T1\*

\*Q1 PNP  
Q2 NPN

### MARKING DIAGRAM



46 = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

| Device         | Package            | Shipping†              |
|----------------|--------------------|------------------------|
| MBT3946DW1T1G  | SC-88<br>(Pb-Free) | 3,000 /<br>Tape & Reel |
| SMBT3946DW1T1G | SC-88<br>(Pb-Free) | 3,000 /<br>Tape & Reel |
| MBT3946DW1T2G  | SC-88<br>(Pb-Free) | 3,000 /<br>Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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**Table 3. ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

| Characteristic                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | Symbol                                                     | Min                                                                | Max                                                        | Unit             |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|--------------------------------------------------------------------|------------------------------------------------------------|------------------|
| <b>OFF CHARACTERISTICS</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |                                                            |                                                                    |                                                            |                  |
| Collector–Emitter Breakdown Voltage (Note 2)<br>( $I_C = 1.0\text{ mA}$ , $I_B = 0$ )<br>( $I_C = -1.0\text{ mA}$ , $I_B = 0$ )                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | (NPN)<br>(PNP)<br>$V_{(BR)CEO}$                            | 40<br>–40                                                          | –<br>–                                                     | Vdc              |
| Collector–Base Breakdown Voltage<br>( $I_C = 10\text{ }\mu\text{A}$ , $I_E = 0$ )<br>( $I_C = -10\text{ }\mu\text{A}$ , $I_E = 0$ )                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | (NPN)<br>(PNP)<br>$V_{(BR)CBO}$                            | 60<br>–40                                                          | –<br>–                                                     | Vdc              |
| Emitter–Base Breakdown Voltage<br>( $I_E = 10\text{ }\mu\text{A}$ , $I_C = 0$ )<br>( $I_E = -10\text{ }\mu\text{A}$ , $I_C = 0$ )                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | (NPN)<br>(PNP)<br>$V_{(BR)EBO}$                            | 6.0<br>–5.0                                                        | –<br>–                                                     | Vdc              |
| Base Cutoff Current<br>( $V_{CE} = 30\text{ Vdc}$ , $V_{EB} = 3.0\text{ Vdc}$ )<br>( $V_{CE} = -30\text{ Vdc}$ , $V_{EB} = -3.0\text{ Vdc}$ )                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | (NPN)<br>(PNP)<br>$I_{BL}$                                 | –<br>–                                                             | 50<br>–50                                                  | nAdc             |
| Collector Cutoff Current<br>( $V_{CE} = 30\text{ Vdc}$ , $V_{EB} = 3.0\text{ Vdc}$ )<br>( $V_{CE} = -30\text{ Vdc}$ , $V_{EB} = -3.0\text{ Vdc}$ )                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | (NPN)<br>(PNP)<br>$I_{CEX}$                                | –<br>–                                                             | 50<br>–50                                                  | nAdc             |
| <b>ON CHARACTERISTICS (Note 2)</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |                                                            |                                                                    |                                                            |                  |
| DC Current Gain<br>( $I_C = 0.1\text{ mA}$ , $V_{CE} = 1.0\text{ Vdc}$ )<br>( $I_C = 1.0\text{ mA}$ , $V_{CE} = 1.0\text{ Vdc}$ )<br>( $I_C = 10\text{ mA}$ , $V_{CE} = 1.0\text{ Vdc}$ )<br>( $I_C = 50\text{ mA}$ , $V_{CE} = 1.0\text{ Vdc}$ )<br>( $I_C = 100\text{ mA}$ , $V_{CE} = 1.0\text{ Vdc}$ )<br><br>( $I_C = -0.1\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ )<br>( $I_C = -1.0\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ )<br>( $I_C = -10\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ )<br>( $I_C = -50\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ )<br>( $I_C = -100\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ ) | (NPN)<br><br><br><br><br><br>(PNP)<br><br><br><br><br><br> | <br>40<br>70<br>100<br>60<br>30<br><br>60<br>80<br>100<br>60<br>30 | <br>–<br>–<br>300<br>–<br>–<br><br>–<br>–<br>300<br>–<br>– | –                |
| Collector–Emitter Saturation Voltage<br>( $I_C = 10\text{ mA}$ , $I_B = 1.0\text{ mA}$ )<br>( $I_C = 50\text{ mA}$ , $I_B = 5.0\text{ mA}$ )<br><br>( $I_C = -10\text{ mA}$ , $I_B = -1.0\text{ mA}$ )<br>( $I_C = -50\text{ mA}$ , $I_B = -5.0\text{ mA}$ )                                                                                                                                                                                                                                                                                                                                                        | (NPN)<br><br><br>(PNP)<br><br><br>                         | <br>–<br>–<br><br>–<br>–                                           | <br>0.2<br>0.3<br><br>–0.25<br>–0.4                        | Vdc              |
| Base–Emitter Saturation Voltage<br>( $I_C = 10\text{ mA}$ , $I_B = 1.0\text{ mA}$ )<br>( $I_C = 50\text{ mA}$ , $I_B = 5.0\text{ mA}$ )<br><br>( $I_C = -10\text{ mA}$ , $I_B = -1.0\text{ mA}$ )<br>( $I_C = -50\text{ mA}$ , $I_B = -5.0\text{ mA}$ )                                                                                                                                                                                                                                                                                                                                                             | (NPN)<br><br><br>(PNP)<br><br><br>                         | <br>0.65<br>–<br><br>–0.65<br>–                                    | <br>0.85<br>0.95<br><br>–0.85<br>–0.95                     | Vdc              |
| <b>SMALL-SIGNAL CHARACTERISTICS</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |                                                            |                                                                    |                                                            |                  |
| Current–Gain – Bandwidth Product<br>( $I_C = 10\text{ mA}$ , $V_{CE} = 20\text{ Vdc}$ , $f = 100\text{ MHz}$ )<br>( $I_C = -10\text{ mA}$ , $V_{CE} = -20\text{ Vdc}$ , $f = 100\text{ MHz}$ )                                                                                                                                                                                                                                                                                                                                                                                                                      | (NPN)<br>(PNP)<br>$f_T$                                    | 300<br>250                                                         | –<br>–                                                     | MHz              |
| Output Capacitance<br>( $V_{CB} = 5.0\text{ Vdc}$ , $I_E = 0$ , $f = 1.0\text{ MHz}$ )<br>( $V_{CB} = -5.0\text{ Vdc}$ , $I_E = 0$ , $f = 1.0\text{ MHz}$ )                                                                                                                                                                                                                                                                                                                                                                                                                                                         | (NPN)<br>(PNP)<br>$C_{obo}$                                | –<br>–                                                             | 4.0<br>4.5                                                 | pF               |
| Input Capacitance<br>( $V_{EB} = 0.5\text{ Vdc}$ , $I_C = 0$ , $f = 1.0\text{ MHz}$ )<br>( $V_{EB} = -0.5\text{ Vdc}$ , $I_C = 0$ , $f = 1.0\text{ MHz}$ )                                                                                                                                                                                                                                                                                                                                                                                                                                                          | (NPN)<br>(PNP)<br>$C_{ibo}$                                | –<br>–                                                             | 8.0<br>10.0                                                | pF               |
| Input Impedance<br>( $V_{CE} = 10\text{ Vdc}$ , $I_C = 1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )<br>( $V_{CE} = -10\text{ Vdc}$ , $I_C = -1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )                                                                                                                                                                                                                                                                                                                                                                                                                                     | (NPN)<br>(PNP)<br>$h_{ie}$                                 | 1.0<br>2.0                                                         | 10<br>12                                                   | k $\Omega$       |
| Voltage Feedback Ratio<br>( $V_{CE} = 10\text{ Vdc}$ , $I_C = 1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )<br>( $V_{CE} = -10\text{ Vdc}$ , $I_C = -1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )                                                                                                                                                                                                                                                                                                                                                                                                                              | (NPN)<br>(PNP)<br>$h_{re}$                                 | 0.5<br>0.1                                                         | 8.0<br>10                                                  | $\times 10^{-4}$ |
| Small–Signal Current Gain<br>( $V_{CE} = 10\text{ Vdc}$ , $I_C = 1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )<br>( $V_{CE} = -10\text{ Vdc}$ , $I_C = -1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )                                                                                                                                                                                                                                                                                                                                                                                                                           | (NPN)<br>(PNP)<br>$h_{fe}$                                 | 100<br>100                                                         | 400<br>400                                                 | –                |

# MBT3946DW1T1G, SMT3946DW1T1G

**Table 4. ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted) (continued)

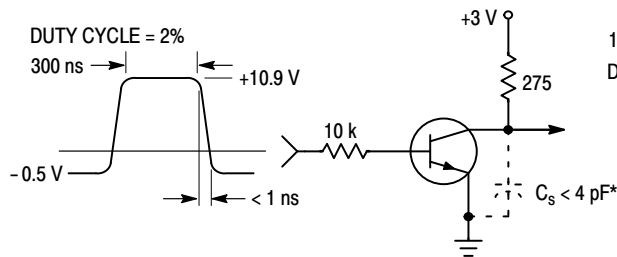
| Characteristic                                                                                                                                                                                                                                             |                | Symbol   | Min        | Max        | Unit             |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|----------|------------|------------|------------------|
| Output Admittance<br>( $V_{CE} = 10\text{ Vdc}$ , $I_C = 1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )<br>( $V_{CE} = -10\text{ Vdc}$ , $I_C = -1.0\text{ mA}$ , $f = 1.0\text{ kHz}$ )                                                                          | (NPN)<br>(PNP) | $h_{oe}$ | 1.0<br>3.0 | 40<br>60   | $\mu\text{mhos}$ |
| Noise Figure<br>( $V_{CE} = 5.0\text{ Vdc}$ , $I_C = 100\text{ }\mu\text{A}$ , $R_S = 1.0\text{ k}\Omega$ , $f = 1.0\text{ kHz}$ )<br>( $V_{CE} = -5.0\text{ Vdc}$ , $I_C = -100\text{ }\mu\text{A}$ , $R_S = 1.0\text{ k}\Omega$ , $f = 1.0\text{ kHz}$ ) | (NPN)<br>(PNP) | NF       | —<br>—     | 5.0<br>4.0 | dB               |

## SWITCHING CHARACTERISTICS

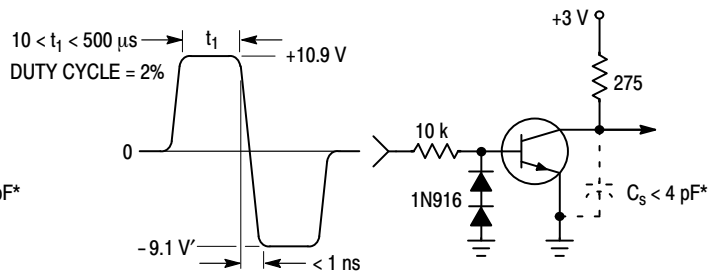
|              |                                                                                                                          |                |       |        |            |    |
|--------------|--------------------------------------------------------------------------------------------------------------------------|----------------|-------|--------|------------|----|
| Delay Time   | ( $V_{CC} = 3.0\text{ Vdc}$ , $V_{BE} = -0.5\text{ Vdc}$ )<br>( $V_{CC} = -3.0\text{ Vdc}$ , $V_{BE} = 0.5\text{ Vdc}$ ) | (NPN)<br>(PNP) | $t_d$ | —<br>— | 35<br>35   | ns |
| Rise Time    | ( $I_C = 10\text{ mA}$ , $I_{B1} = 1.0\text{ mA}$ )<br>( $I_C = -10\text{ mA}$ , $I_{B1} = -1.0\text{ mA}$ )             | (NPN)<br>(PNP) | $t_r$ | —<br>— | 35<br>35   |    |
| Storage Time | ( $V_{CC} = 3.0\text{ Vdc}$ , $I_C = 10\text{ mA}$ )<br>( $V_{CC} = -3.0\text{ Vdc}$ , $I_C = -10\text{ mA}$ )           | (NPN)<br>(PNP) | $t_s$ | —<br>— | 200<br>225 | ns |
| Fall Time    | ( $I_{B1} = I_{B2} = 1.0\text{ mA}$ )<br>( $I_{B1} = I_{B2} = -1.0\text{ mA}$ )                                          | (NPN)<br>(PNP) | $t_f$ | —<br>— | 50<br>75   |    |

2. Pulse Test: Pulse Width  $\leq 300\text{ }\mu\text{s}$ ; Duty Cycle  $\leq 2.0\%$ .

### (NPN)



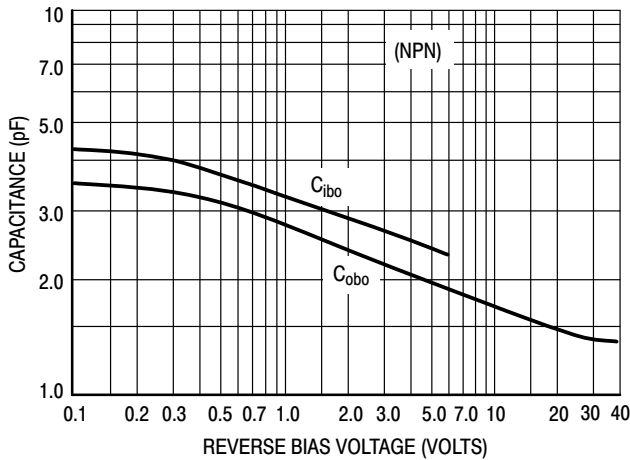
**Figure 1. Delay and Rise Time Equivalent Test Circuit**



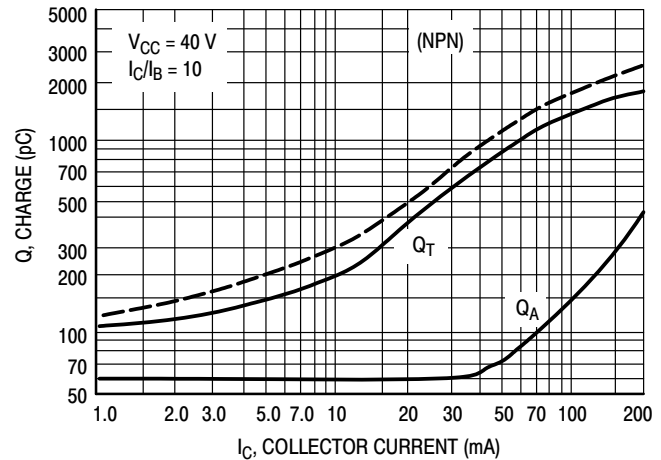
**Figure 2. Storage and Fall Time Equivalent Test Circuit**

## TYPICAL TRANSIENT CHARACTERISTICS

—  $T_J = 25^\circ\text{C}$   
- -  $T_J = 125^\circ\text{C}$



**Figure 3. Capacitance**



**Figure 4. Charge Data**

# MBT3946DW1T1G, SMT3946DW1T1G

(NPN)

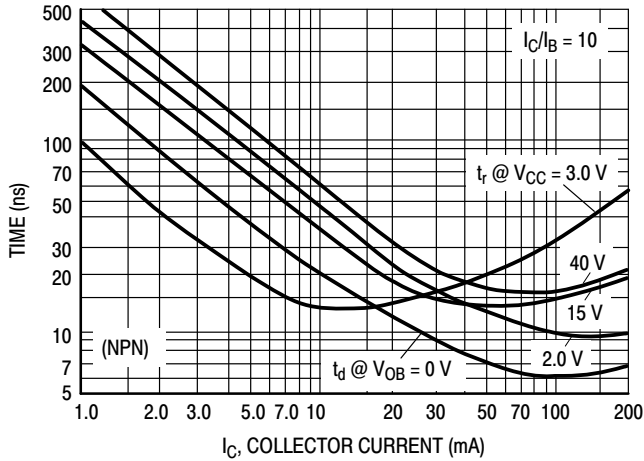


Figure 5. Turn-On Time

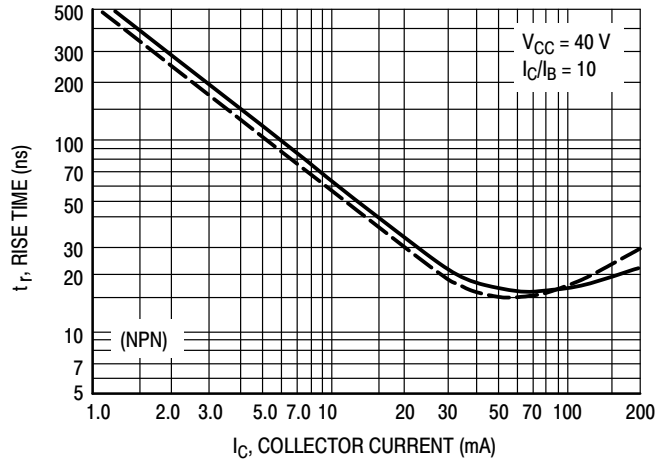


Figure 6. Rise Time

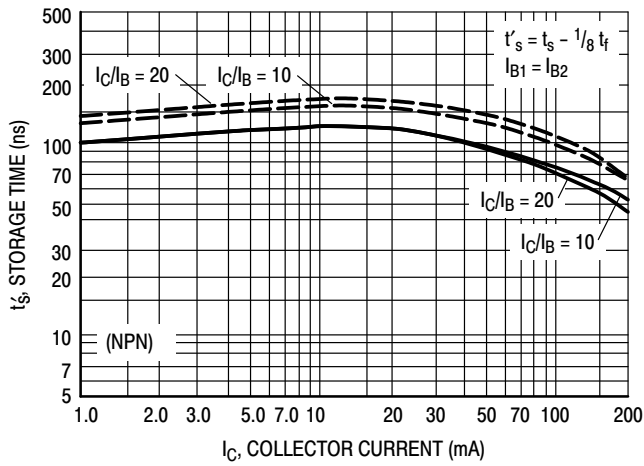


Figure 7. Storage Time

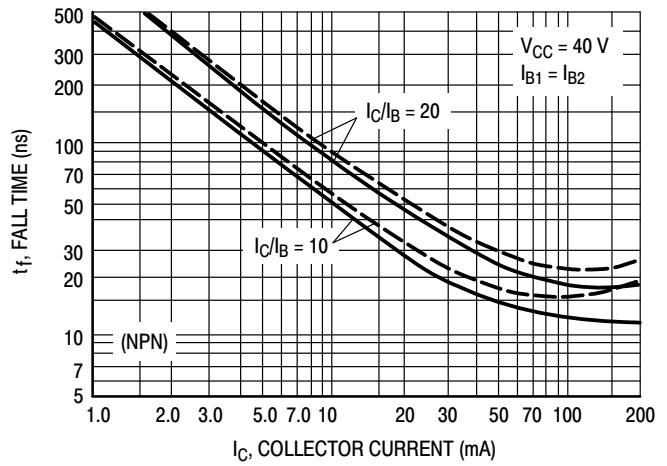


Figure 8. Fall Time

## TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

( $V_{CE} = 5.0$  Vdc,  $T_A = 25^\circ\text{C}$ , Bandwidth = 1.0 Hz)

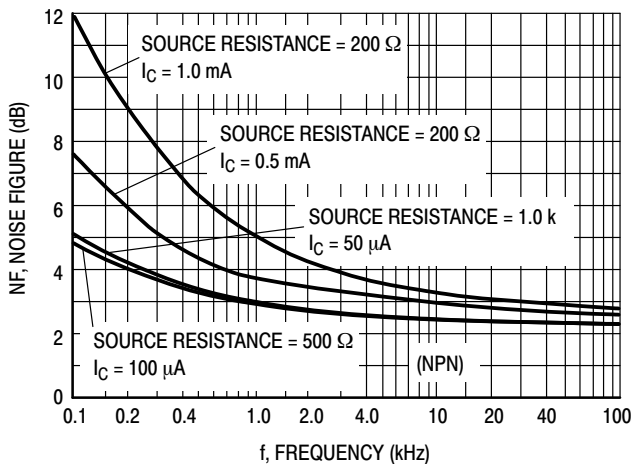


Figure 9. Noise Figure

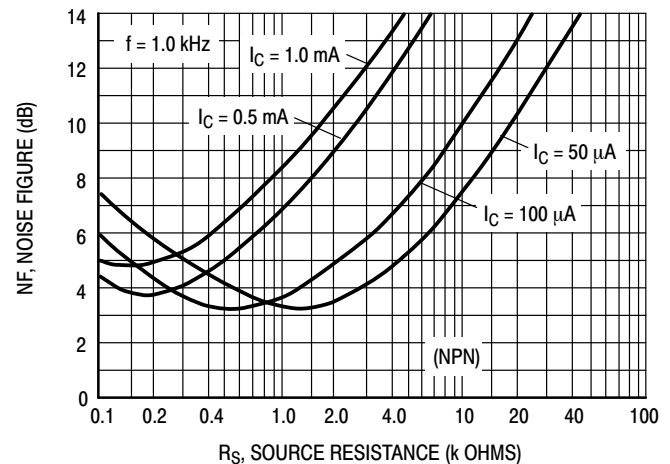


Figure 10. Noise Figure

# MBT3946DW1T1G, SMBT3946DW1T1G

## (NPN) h PARAMETERS

( $V_{CE} = 10 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ ,  $T_A = 25^\circ\text{C}$ )

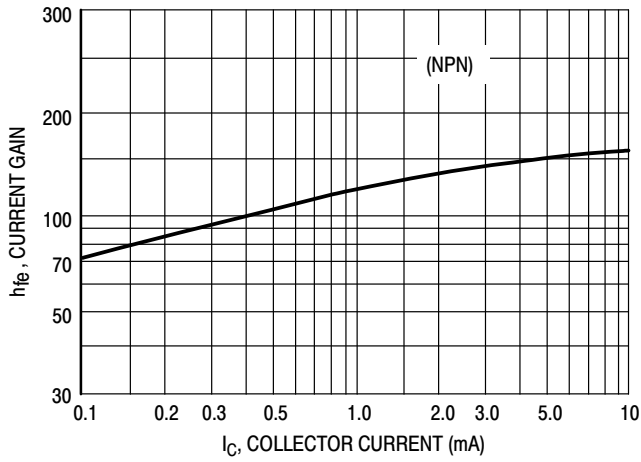


Figure 11. Current Gain

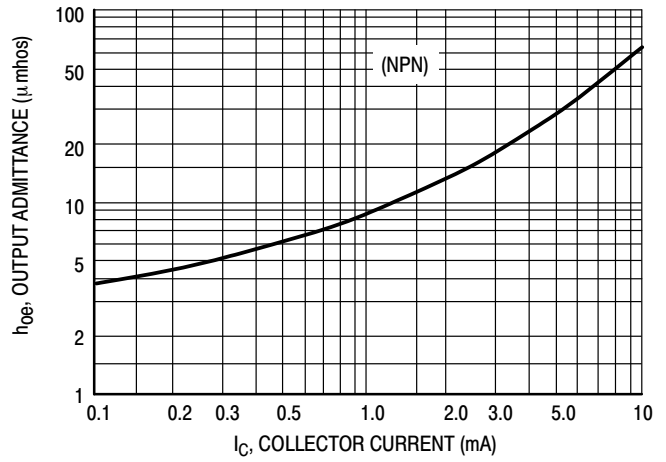


Figure 12. Output Admittance

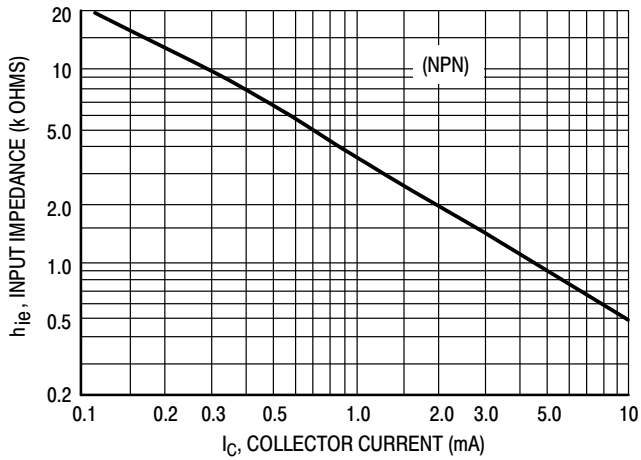


Figure 13. Input Impedance

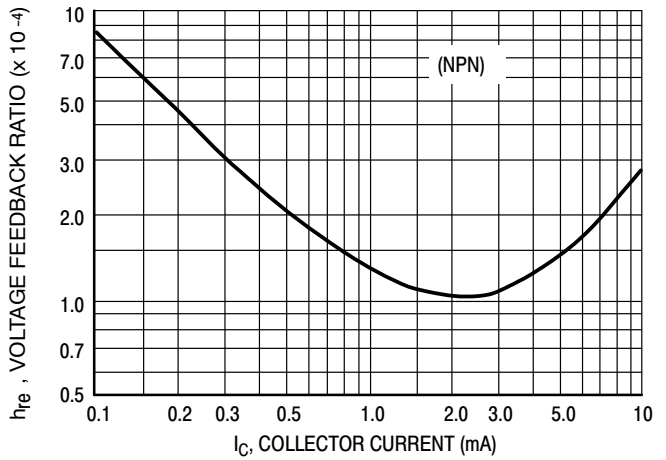


Figure 14. Voltage Feedback Ratio

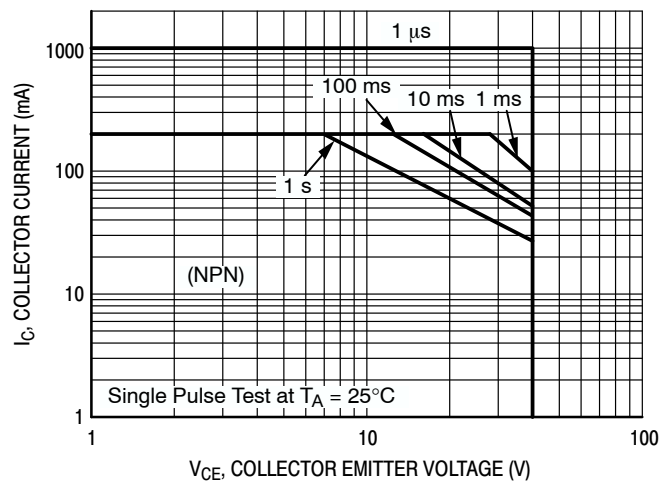


Figure 15. Safe Operating Area

# MBT3946DW1T1G, SMBT3946DW1T1G

## (NPN) TYPICAL STATIC CHARACTERISTICS

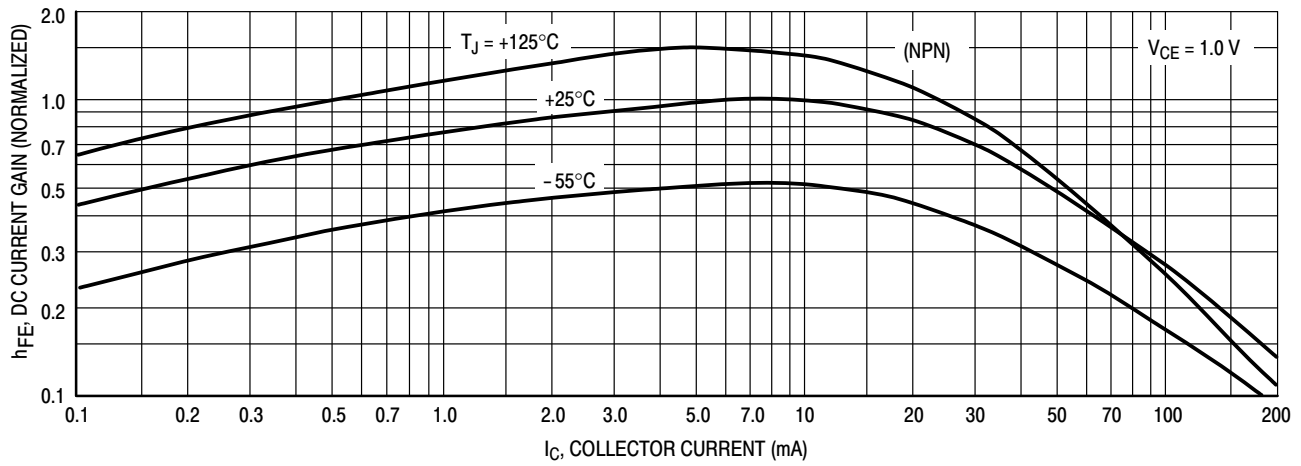


Figure 16. DC Current Gain

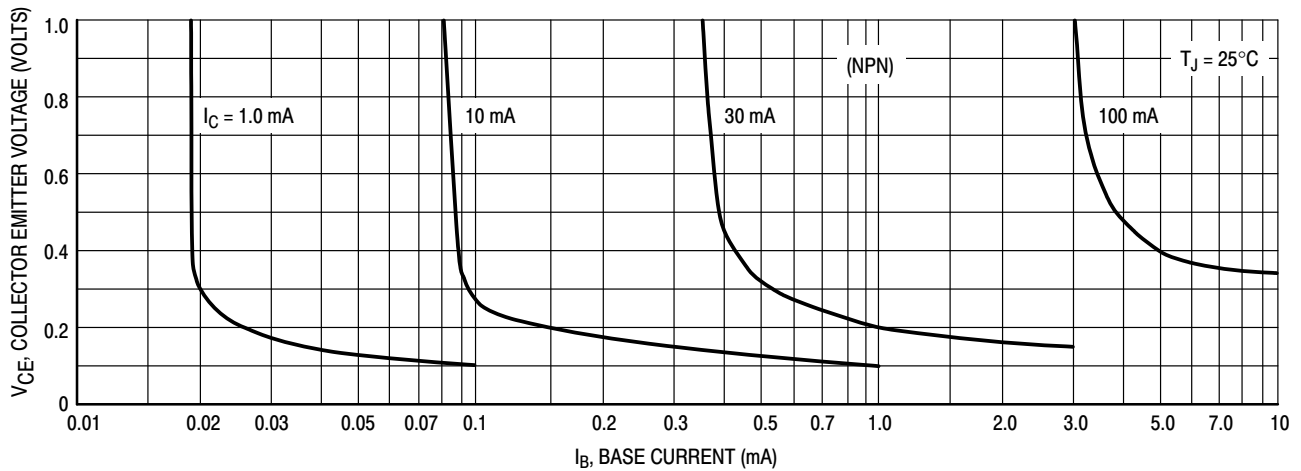


Figure 17. Collector Saturation Region

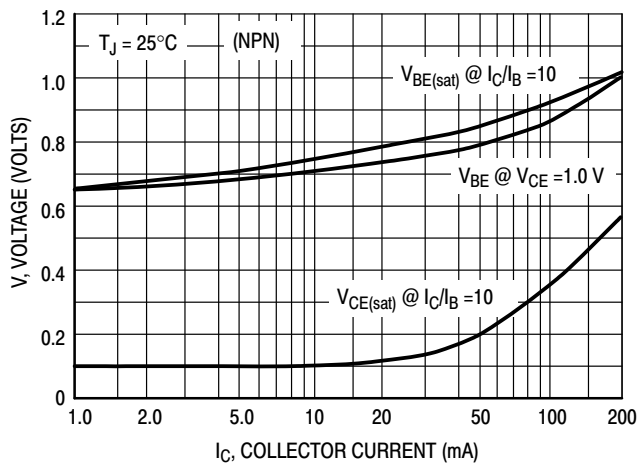


Figure 18. "ON" Voltages

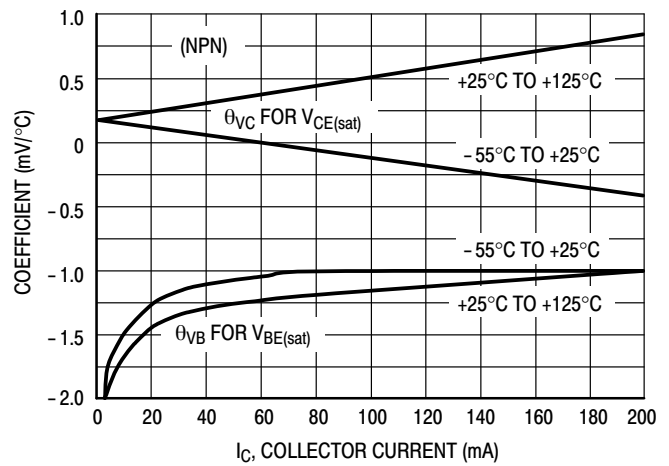


Figure 19. Temperature Coefficients

# MBT3946DW1T1G, SMBT3946DW1T1G

(PNP)

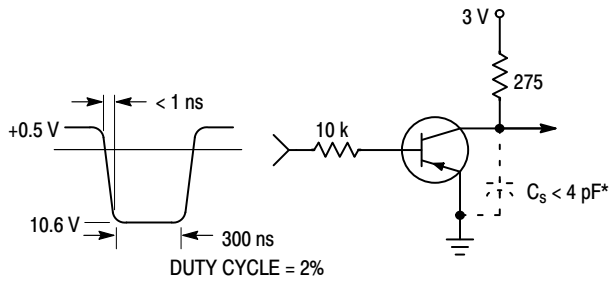


Figure 20. Delay and Rise Time Equivalent Test Circuit

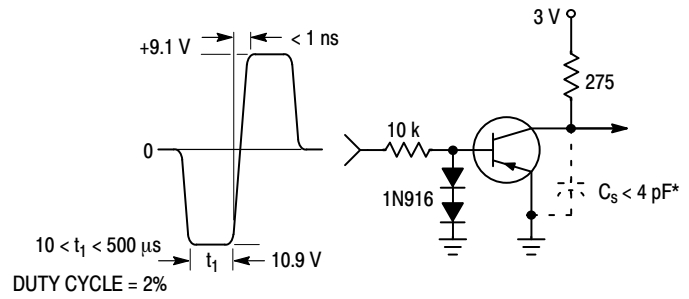


Figure 21. Storage and Fall Time Equivalent Test Circuit

\* Total shunt capacitance of test jig and connectors

## TYPICAL TRANSIENT CHARACTERISTICS

—  $T_J = 25^\circ\text{C}$   
 - -  $T_J = 125^\circ\text{C}$

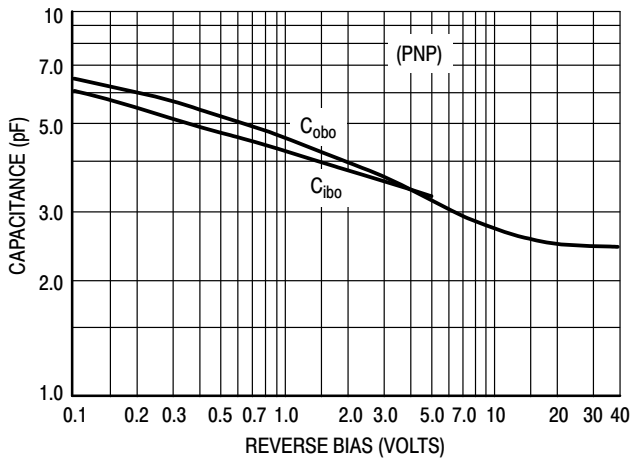


Figure 22. Capacitance

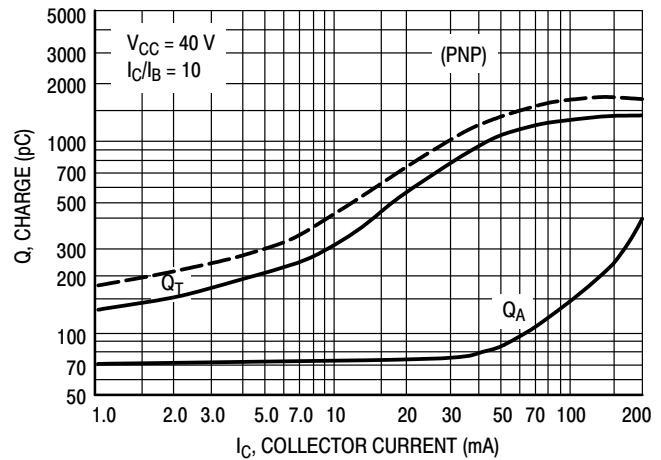


Figure 23. Charge Data

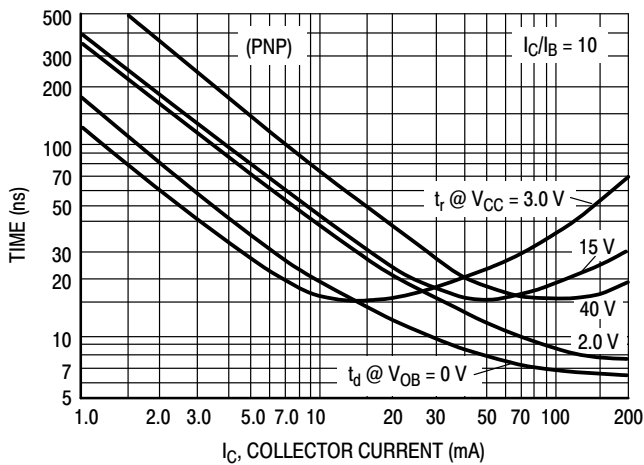


Figure 24. Turn-On Time

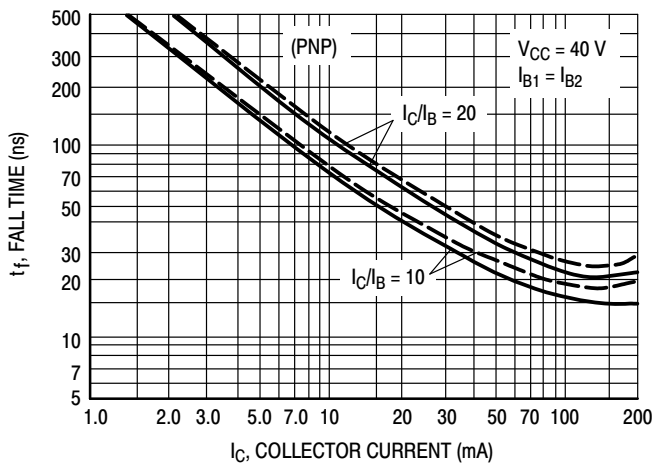


Figure 25. Fall Time

# MBT3946DW1T1G, SMT3946DW1T1G

(PNP)

## TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

( $V_{CE} = -5.0$  Vdc,  $T_A = 25^\circ\text{C}$ , Bandwidth = 1.0 Hz)

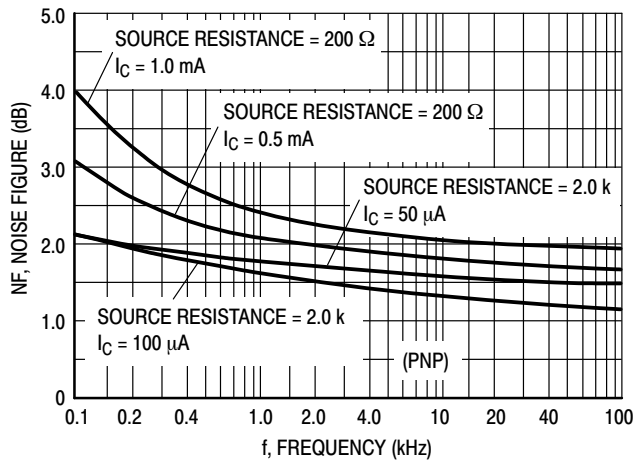


Figure 26.

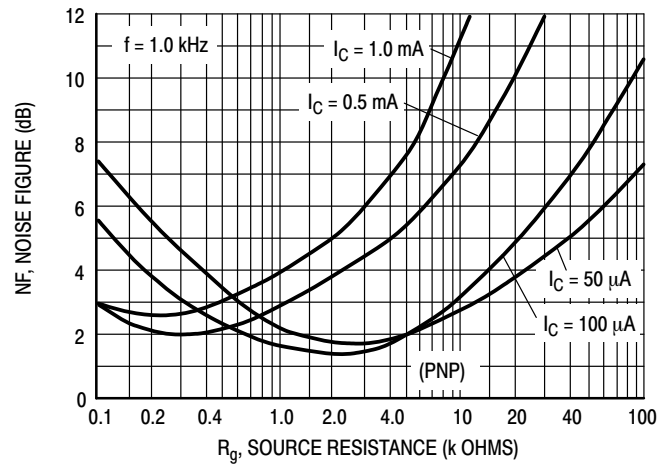


Figure 27.

## h PARAMETERS

( $V_{CE} = -10$  Vdc,  $f = 1.0$  kHz,  $T_A = 25^\circ\text{C}$ )

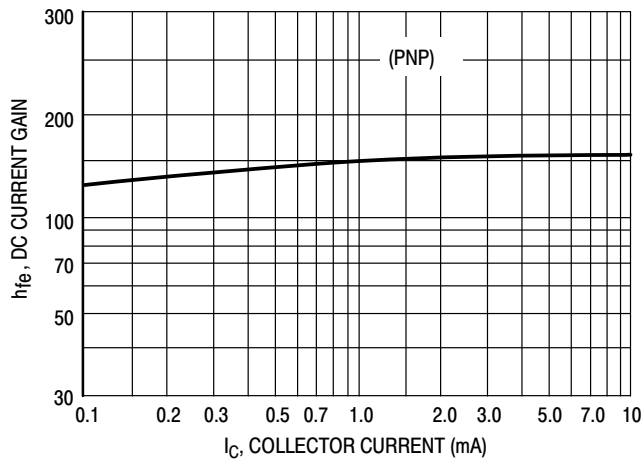


Figure 28. Current Gain

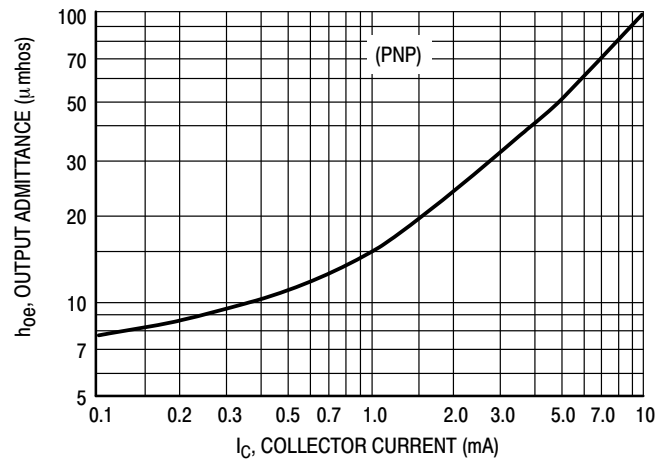


Figure 29. Output Admittance

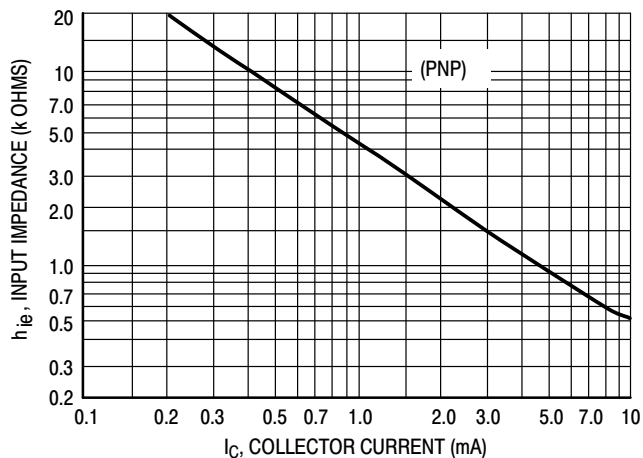


Figure 30. Input Impedance

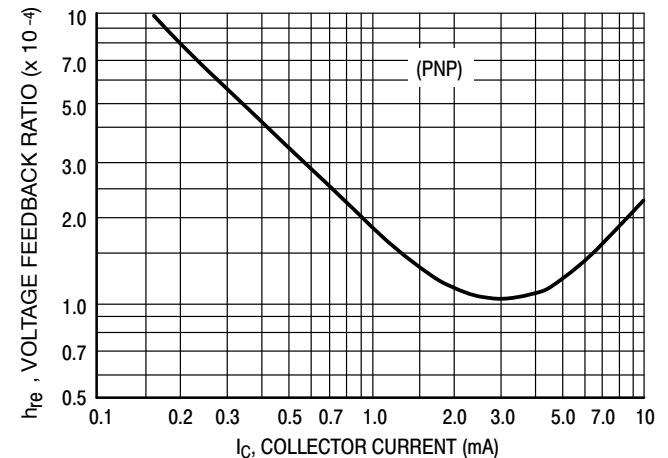


Figure 31. Voltage Feedback Ratio



# MBT3946DW1T1G, SMBT3946DW1T1G

(PNP)

## TYPICAL STATIC CHARACTERISTICS

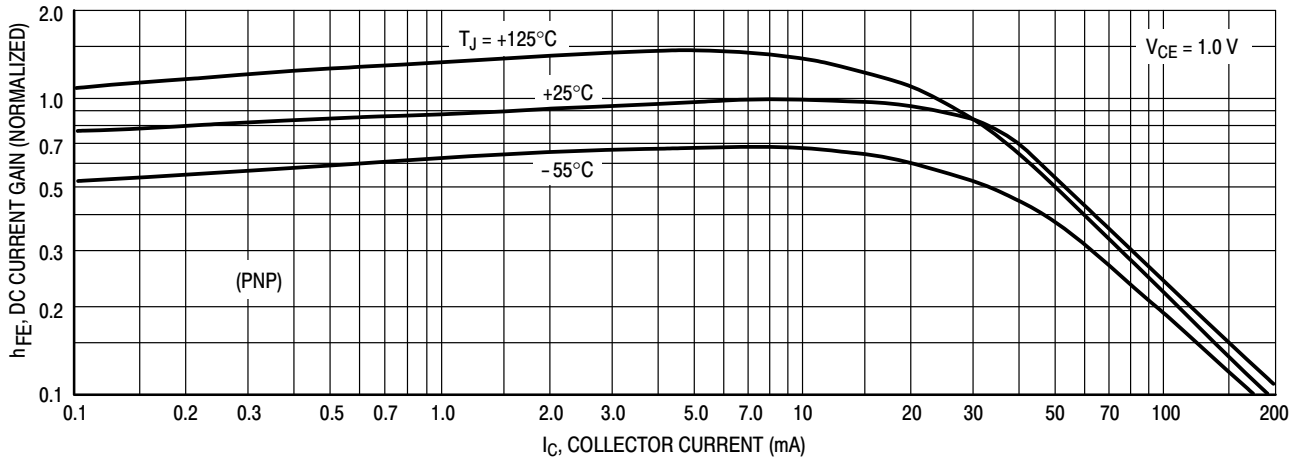


Figure 32. DC Current Gain

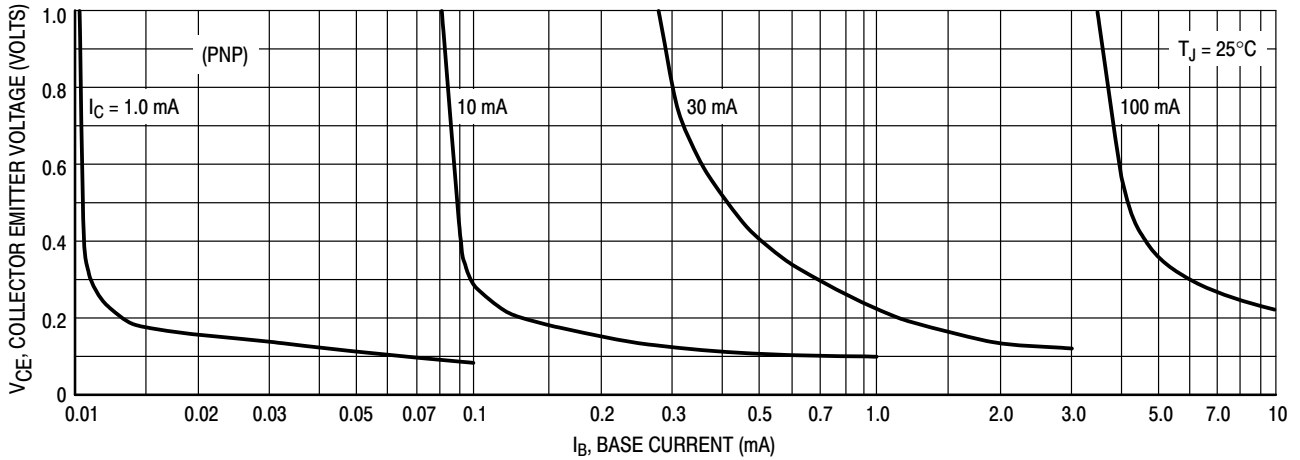


Figure 33. Collector Saturation Region

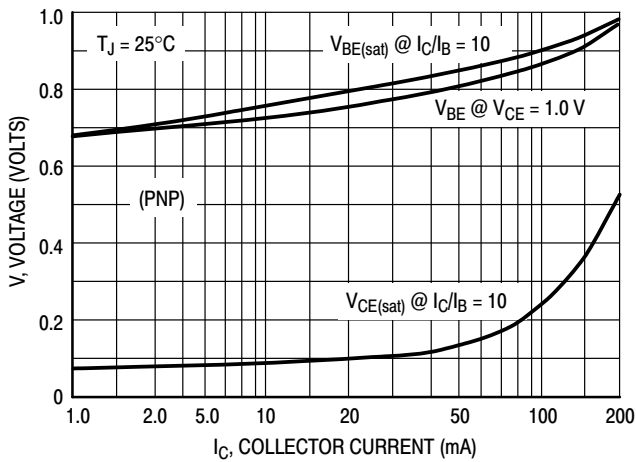


Figure 34. "ON" Voltages

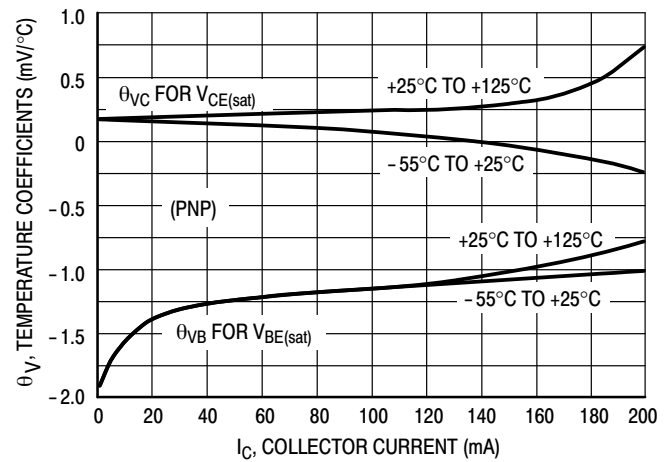
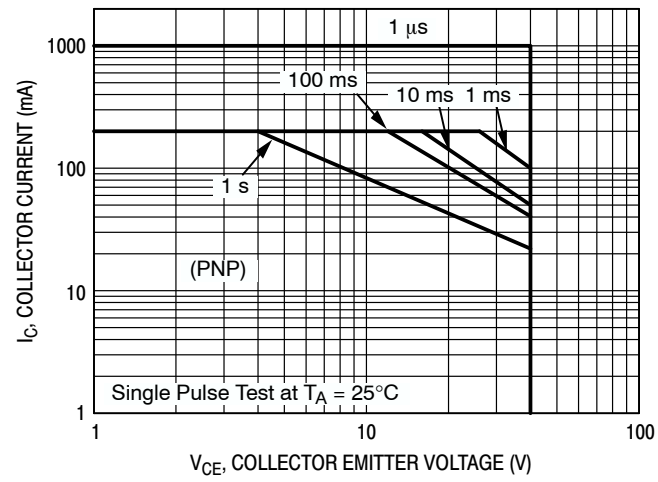


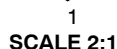
Figure 35. Temperature Coefficients

# MBT3946DW1T1G, SMT3946DW1T1G



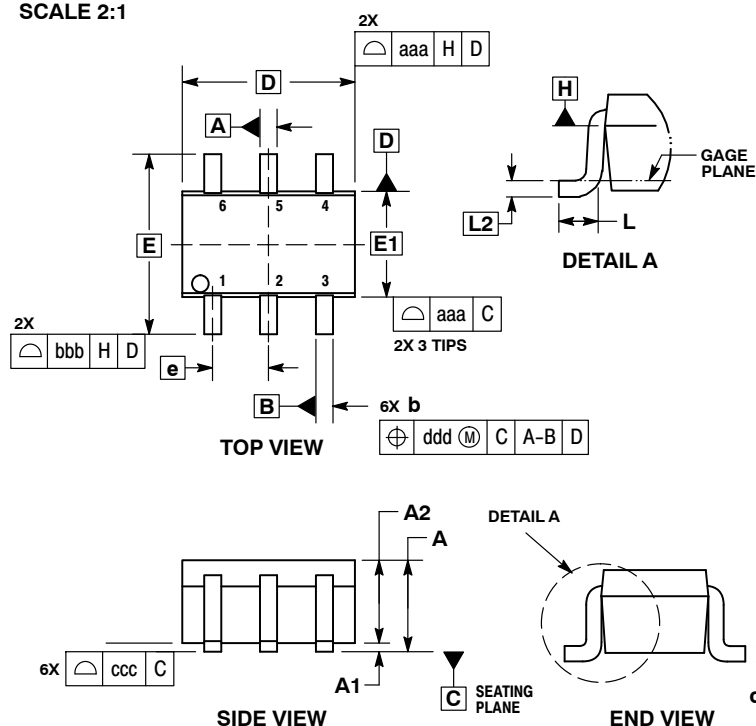
**Figure 36. Safe Operating Area**

**ON**



**SC-88/SC70-6/SOT-363**  
CASE 419B-02  
ISSUE Y

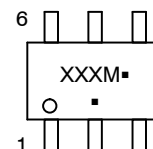
DATE 11 DEC 2012



- NOTES:**
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
  4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
  5. DATUMS A AND B ARE DETERMINED AT DATUM H.
  6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
  7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

| DIM        | MILLIMETERS |      |      | INCHES    |       |       |
|------------|-------------|------|------|-----------|-------|-------|
|            | MIN         | NOM  | MAX  | MIN       | NOM   | MAX   |
| <b>A</b>   | ---         | ---  | 1.10 | ---       | ---   | 0.043 |
| <b>A1</b>  | 0.00        | ---  | 0.10 | 0.000     | ---   | 0.004 |
| <b>A2</b>  | 0.70        | 0.90 | 1.00 | 0.027     | 0.035 | 0.039 |
| <b>b</b>   | 0.15        | 0.20 | 0.25 | 0.006     | 0.008 | 0.010 |
| <b>C</b>   | 0.08        | 0.15 | 0.22 | 0.003     | 0.006 | 0.009 |
| <b>D</b>   | 1.80        | 2.00 | 2.20 | 0.070     | 0.078 | 0.086 |
| <b>E</b>   | 2.00        | 2.10 | 2.20 | 0.078     | 0.082 | 0.086 |
| <b>E1</b>  | 1.15        | 1.25 | 1.35 | 0.045     | 0.049 | 0.053 |
| <b>e</b>   | 0.65 BSC    |      |      | 0.026 BSC |       |       |
| <b>L</b>   | 0.26        | 0.36 | 0.46 | 0.010     | 0.014 | 0.018 |
| <b>L2</b>  | 0.15 BSC    |      |      | 0.006 BSC |       |       |
| <b>aaa</b> | 0.15        |      |      | 0.006     |       |       |
| <b>bbb</b> | 0.30        |      |      | 0.012     |       |       |
| <b>ccc</b> | 0.10        |      |      | 0.004     |       |       |
| <b>ddd</b> | 0.10        |      |      | 0.004     |       |       |

### GENERIC MARKING DIAGRAM\*



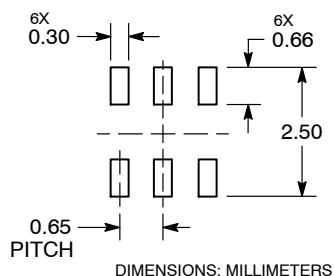
XXX = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

### RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## STYLES ON PAGE 2

|                         |                             |                                                                                                                                                                                     |
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**SC-88/SC70-6/SOT-363**  
**CASE 419B-02**  
**ISSUE Y**

DATE 11 DEC 2012

|                                                                                                                   |                                                                                                               |                                                                                                                    |                                                                                                                    |                                                                                                                    |                                                                                                              |
|-------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------|
| <b>STYLE 1:</b><br>PIN 1. EMITTER 2<br>2. BASE 2<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. BASE 1<br>6. COLLECTOR 2 | <b>STYLE 2:</b><br>CANCELLED                                                                                  | <b>STYLE 3:</b><br>CANCELLED                                                                                       | <b>STYLE 4:</b><br>PIN 1. CATHODE<br>2. CATHODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. ANODE               | <b>STYLE 5:</b><br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | <b>STYLE 6:</b><br>PIN 1. ANODE 2<br>2. N/C<br>3. CATHODE 1<br>4. ANODE 1<br>5. N/C<br>6. CATHODE 2          |
| <b>STYLE 7:</b><br>PIN 1. SOURCE 2<br>2. DRAIN 2<br>3. GATE 1<br>4. SOURCE 1<br>5. DRAIN 1<br>6. GATE 2           | <b>STYLE 8:</b><br>CANCELLED                                                                                  | <b>STYLE 9:</b><br>PIN 1. EMITTER 2<br>2. EMITTER 1<br>3. COLLECTOR 1<br>4. BASE 1<br>5. BASE 2<br>6. COLLECTOR 2  | <b>STYLE 10:</b><br>PIN 1. SOURCE 2<br>2. SOURCE 1<br>3. GATE 1<br>4. DRAIN 1<br>5. DRAIN 2<br>6. GATE 2           | <b>STYLE 11:</b><br>PIN 1. CATHODE 2<br>2. CATHODE 2<br>3. ANODE 1<br>4. CATHODE 1<br>5. CATHODE 1<br>6. ANODE 2   | <b>STYLE 12:</b><br>PIN 1. ANODE 2<br>2. ANODE 2<br>3. CATHODE 1<br>4. ANODE 1<br>5. ANODE 1<br>6. CATHODE 2 |
| <b>STYLE 13:</b><br>PIN 1. ANODE<br>2. N/C<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | <b>STYLE 14:</b><br>PIN 1. VREF<br>2. GND<br>3. GND<br>4. IOUT<br>5. VEN<br>6. VCC                            | <b>STYLE 15:</b><br>PIN 1. ANODE 1<br>2. ANODE 2<br>3. ANODE 3<br>4. CATHODE 3<br>5. CATHODE 2<br>6. CATHODE 1     | <b>STYLE 16:</b><br>PIN 1. BASE 1<br>2. EMITTER 2<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 1<br>6. COLLECTOR 1 | <b>STYLE 17:</b><br>PIN 1. BASE 1<br>2. EMITTER 1<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 2<br>6. COLLECTOR 1 | <b>STYLE 18:</b><br>PIN 1. VIN1<br>2. VCC<br>3. VOUT2<br>4. VIN2<br>5. GND<br>6. VOUT1                       |
| <b>STYLE 19:</b><br>PIN 1. IOUT<br>2. GND<br>3. GND<br>4. V CC<br>5. V EN<br>6. V REF                             | <b>STYLE 20:</b><br>PIN 1. COLLECTOR<br>2. COLLECTOR<br>3. BASE<br>4. EMITTER<br>5. COLLECTOR<br>6. COLLECTOR | <b>STYLE 21:</b><br>PIN 1. ANODE 1<br>2. N/C<br>3. ANODE 2<br>4. CATHODE 2<br>5. N/C<br>6. CATHODE 1               | <b>STYLE 22:</b><br>PIN 1. D1 (i)<br>2. GND<br>3. D2 (i)<br>4. D2 (c)<br>5. VBUS<br>6. D1 (c)                      | <b>STYLE 23:</b><br>PIN 1. Vn<br>2. CH1<br>3. Vp<br>4. N/C<br>5. CH2<br>6. N/C                                     | <b>STYLE 24:</b><br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE<br>4. CATHODE<br>5. CATHODE<br>6. CATHODE       |
| <b>STYLE 25:</b><br>PIN 1. BASE 1<br>2. CATHODE<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER<br>6. COLLECTOR 1    | <b>STYLE 26:</b><br>PIN 1. SOURCE 1<br>2. GATE 1<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 2<br>6. DRAIN 1      | <b>STYLE 27:</b><br>PIN 1. BASE 2<br>2. BASE 1<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. EMITTER 2<br>6. COLLECTOR 2 | <b>STYLE 28:</b><br>PIN 1. DRAIN<br>2. DRAIN<br>3. GATE<br>4. SOURCE<br>5. DRAIN<br>6. DRAIN                       | <b>STYLE 29:</b><br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE/ANODE<br>6. CATHODE          | <b>STYLE 30:</b><br>PIN 1. SOURCE 1<br>2. DRAIN 2<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 1<br>6. DRAIN 1    |

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

|                         |                             |                                                                                                                                                                                     |
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